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	Filing Date		2005-05-24	
	First Named Inventor	Bruce B. Doris		
	Art Unit	2826		
	Examiner Name	Benjamin T. Liu		
	Attorney Docket Number	6798		

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	1	A. Shimizu et al., "Local Mechanical-Stress Control (LMC): A New Technique for CMOS-Performance Enhancement", International Electron Devices Meeting 2001. IEDM Technical Digest. 2001 IEEE, pp. 19.4.1-19.4.4.	<input type="checkbox"/>
	2	S. Ito et al., "Effect of Mechanical Stress Induced by Etch-Stop Nitride: Impact on Deep-Submicron Transistor Performance", Microelectronics and Reliability, Elsevier Science Ltd., GB, vol. 42, no. 2, February 2002, pages 201-209.	<input type="checkbox"/>
	3	S. Ito et al., "Mechanical Stress Effect of Etch-Stop Nitride and its Impact on Deep Submicron Transistor Design", IEDM Technical Digest, 2000 IEEE, pp. 10.7.1-10.7.4.	<input type="checkbox"/>

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CERTIFICATION STATEMENT

Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

☐ That each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(1).

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☐ See attached certification statement.

☐ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

☒ None

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Joseph P. Abate/	Date (YYYY-MM-DD)	2006-08-14
Name/Print	Joseph P. Abate	Registration Number	30238

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